

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): CHEN et al.

Serial No.: 10/054,373

Filed: January 22, 2002

Title: Semiconductor Processing Chamber  
Substrate Holder Method and  
Structure

Attorney Docket No.: AIBT-I2K09

DEC 11 2003  
PATENTS & TRADEMARK OFFICE

Group Art Unit: 2831

Examiner: Harris, A.

5

RESPONSE TO OFFICE ACTION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

10

A. Introductory Comments

15 The following is submitted in response to the Office Action dated October 10, 2003, and  
is currently due January 10, 2004.

20

25

I hereby certify that this correspondence is being

37 C.F.R. §1.8

30  transmitted via facsimile to the United States Patent and Trademark Office to fax number: \_\_\_\_\_  
Date of Transmittal: \_\_\_\_\_  
 deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450.

35 Date of Deposit: DECEMBER 9, 2003  
Typed/Printed Name: Bradley T. Sako

40 Signature: Tom Sako

12/12/2003 SSESHE1 0000057 10054373  
01 FC:1202

18.00 00

RECEIVED  
DEC 17 2003  
TECHNOLOGY CENTER 2800